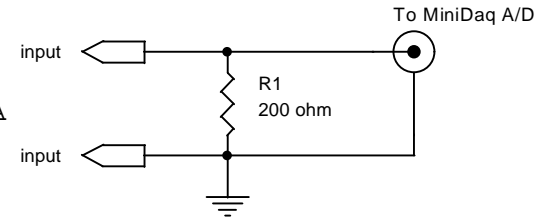


$0V \leq V_{in} \leq V_{max}$

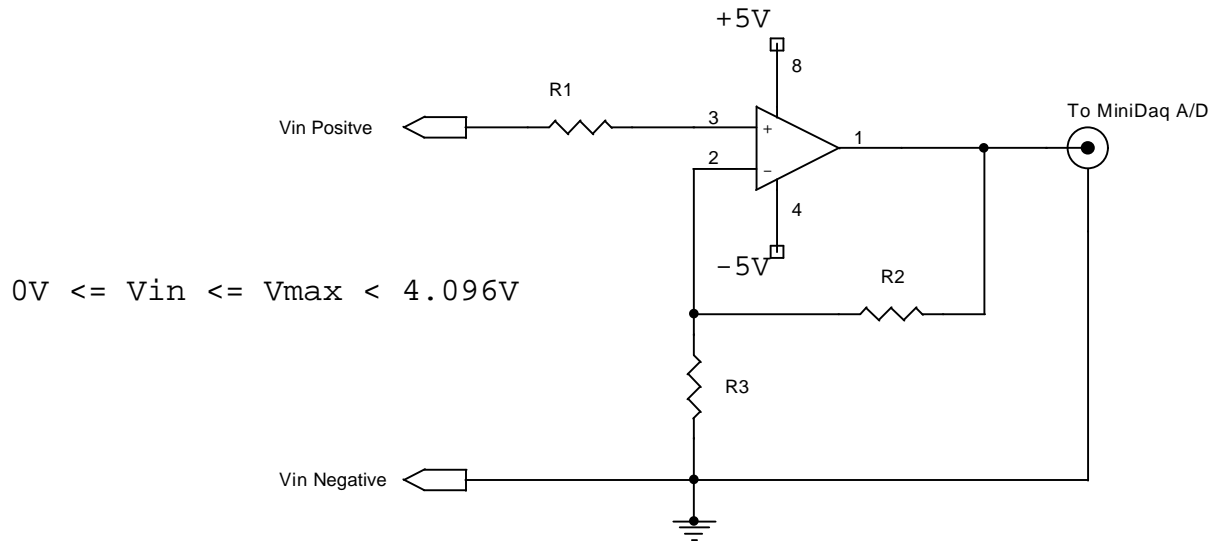
$$4.096 = V_{max} * R2 / (R1 + R2)$$

By using a resistor voltage divider, larger input signals can be scaled down to fit MiniDaqs input voltage range.

$$I = 4 - 20mA$$



4 - 20ma current loop can be read through a resistive shunt



$0V \leq V_{in} \leq V_{max} < 4.096V$

$$4.096 = V_{max} * (R2/R1 + 1) \quad ; \quad R3=R1$$

By using a Op-Amp, small input signals can be amplified.

MiniDaq A/D
Input
Circuits

Note: MiniDaq set to Unipolar input